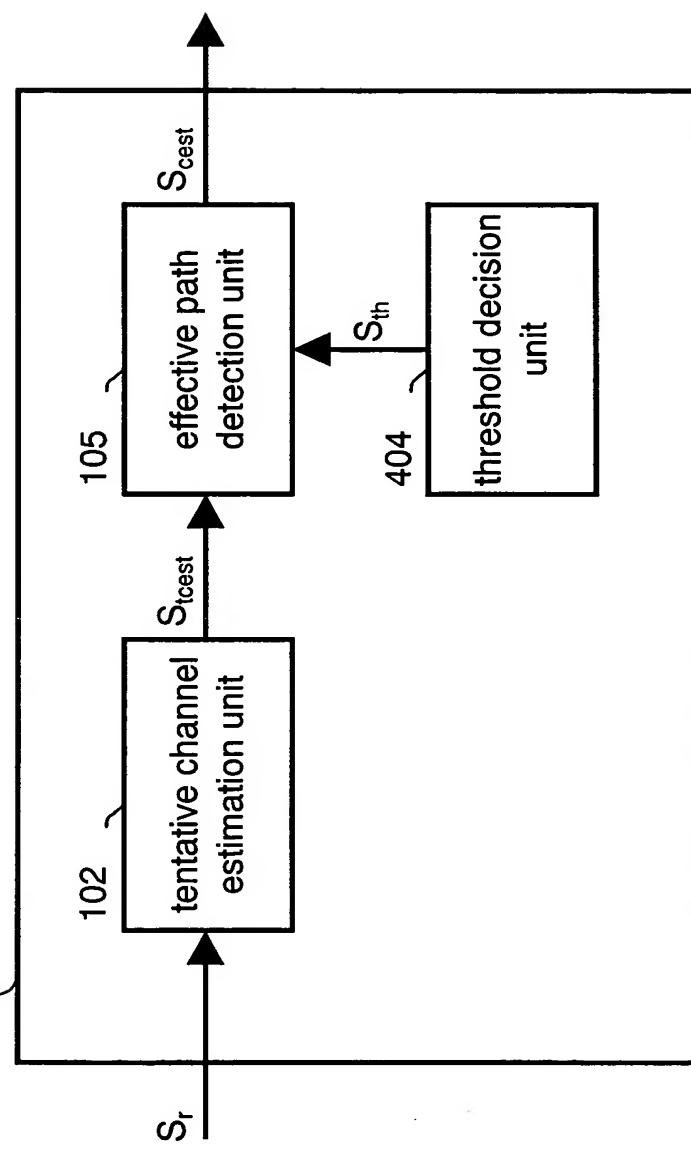


Fig.1  
401 channel estimation circuit



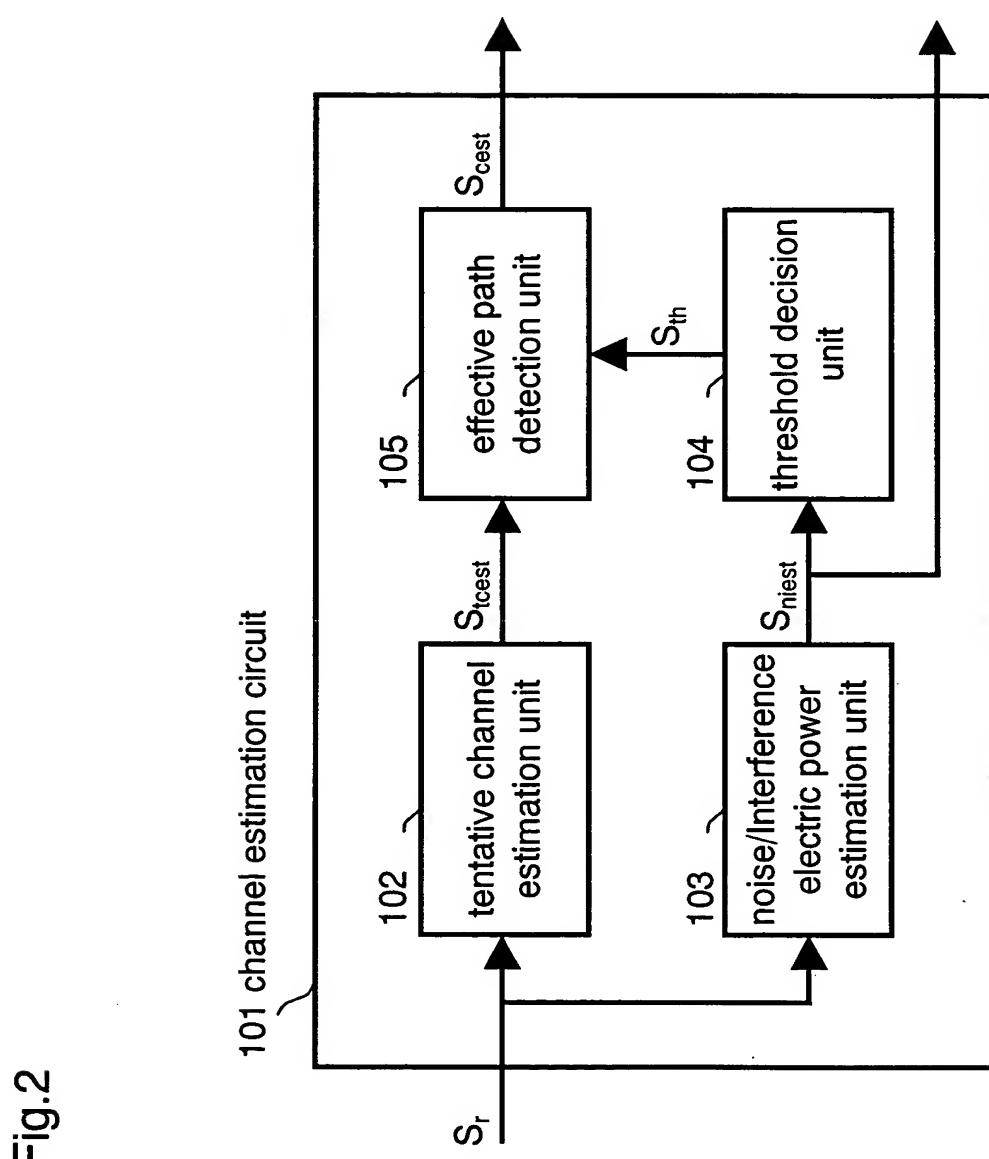


Fig.3  
201 channel estimation circuit

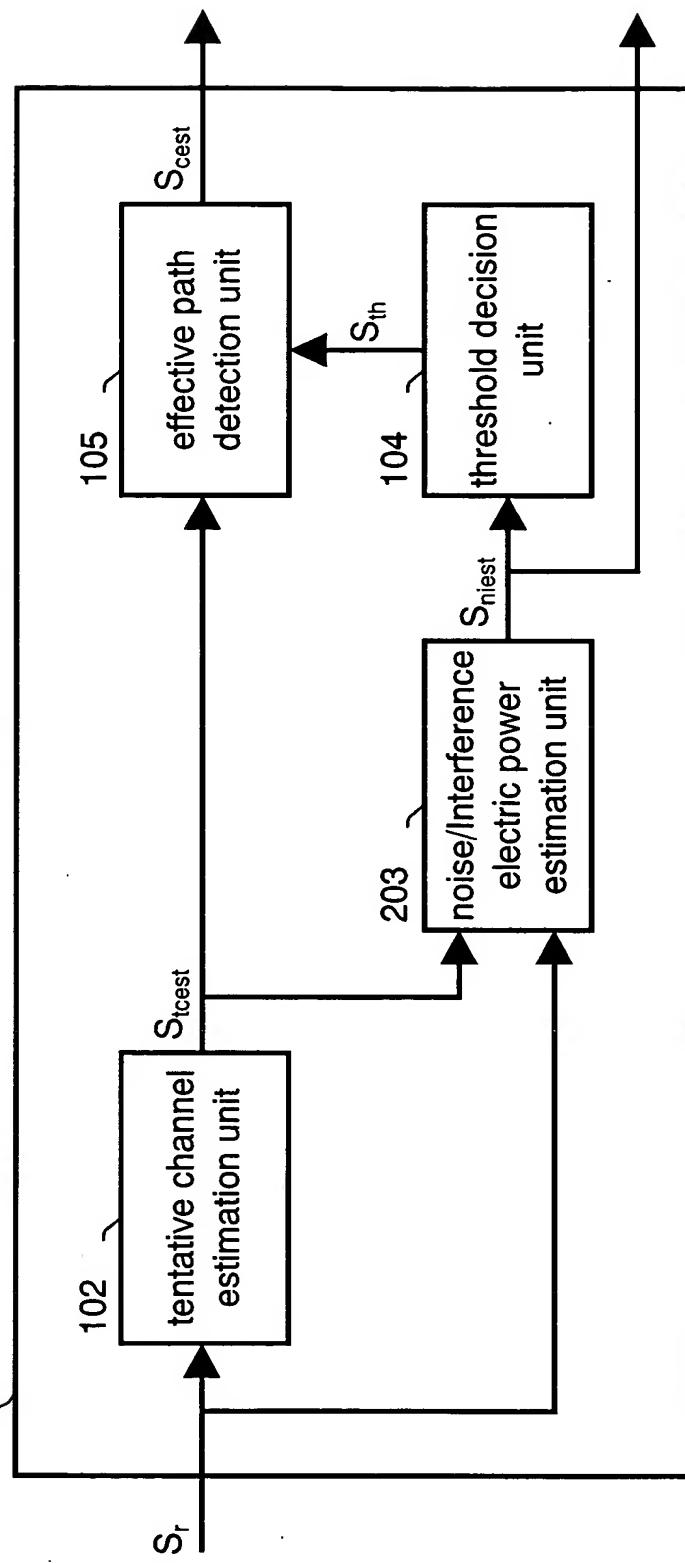


Fig. 4  
301 channel estimation circuit

